

## **SOT-89 Plastic-Encapsulate Transistors**

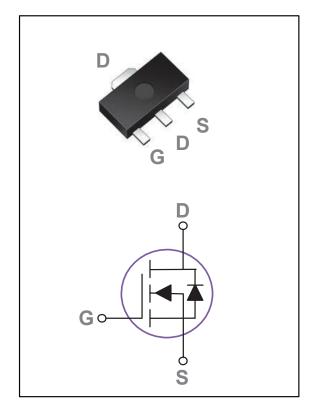
BVDSS	RDSON	ID
100V	350mΩ	3A

### **Features**

- 100V,3A , RDS(ON)=350mΩ@VGS=10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

## **Applications**

- Networking
- Load Switch
- LED applications



#### **MAXIMUM RATINGS AND CHARACTERISTICS**

@ 25°C Ambient Temperature (unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	$V_{GS}$	±20	V
Drain Current – Continuous (Tc=25°C)	Ip	3	А
Drain Current – Continuous (Tc=100°C)	UI	1.9	А
Drain Current – Pulsed <sup>1</sup>	I <sub>DM</sub>	12	А
Single Pulse Avalanche Energy <sup>2</sup>	EAS	6	mJ
Single Pulse Avalanched Current <sup>2</sup>	IAS	11	А
Power Dissipation (Tc=25°C)	PD	6.9	W
Power Dissipation – Derate above 25℃	PD	0.06	W/°C
Storage Temperature Range	T <sub>STG</sub>	-50 to 150	
Operating Junction Temperature Range	TJ	-50 to 150	℃

## **Thermal Characteristics**

Parameter	Symbol	Тур.	Max.	Unit
Thermal Resistance Junction to ambient	Reja		80	°C/W
Thermal Resistance Junction to Case	R <sub>θJC</sub>		18	°C/W



## MOSFET ELECTRICAL CHARACTERISTICS TA=25°C unless otherwise specified

Parameter	Symbol	ool Conditions		Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub> V <sub>GS</sub> =0V , I <sub>D</sub> =250uA		100			V
BV <sub>DSS</sub> Temperature Coefficient	△BV <sub>DSS</sub> /△T <sub>J</sub>	Reference to 25℃, I <sub>D</sub> =1mA		0.09		V/℃
Dunin Course Legland Commant		V <sub>DS</sub> =100V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	uA
Drain-Source Leakage Current	IDSS	V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , T <sub>J</sub> =125℃			10	uA
Gate-Source Leakage Current	Igss	$V_{GS}$ =±20V , $V_{DS}$ =0V			±100	nA

#### **On Characteristics**

Static Drain-Source On-Resistance	D	V <sub>GS</sub> =10V , I <sub>D</sub> =2A		280	350	mΩ
Static Dialii-Source Off-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =1A		290	360	mΩ
Gate Threshold Voltage	V <sub>GS(th)</sub>	V V L 050 A		1.8	2.5	V
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}$	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-4.4		mV/°C
Forward Transconductance	gfs	V <sub>DS</sub> =10V , I <sub>D</sub> =2A		3		S

## **Dynamic and switching Characteristics**

Total Gate Charge <sup>3, 4</sup>	Qg		 5.8	11	
Gate-Source Charge <sup>3, 4</sup>	Qgs	$V_{DS}$ =50V , $V_{GS}$ =10V , $I_{D}$ =1A	 0.7	3	nC
Gate-Drain Charge <sup>3, 4</sup>	$Q_{gd}$		 2.5	5	
Turn-On Delay Time <sup>3, 4</sup>	T <sub>d(on)</sub>		 5.2	10	
Rise Time <sup>3, 4</sup>	Tr	$V_{DD}$ =50V , $V_{GS}$ =10V ,	 6.8	12	I
Turn-Off Delay Time <sup>3, 4</sup>	T <sub>d(off)</sub>	$R_G=3.3\Omega$ $I_D=1A$	 14.5	28	ns
Fall Time <sup>3 , 4</sup>	T <sub>f</sub>		 2.1	5	
Input Capacitance	Ciss		 480	960	
Output Capacitance	Coss	$V_{DS}$ =50V , $V_{GS}$ =0V , $F$ =1MHz	 25	50	pF
Reverse Transfer Capacitance	Crss		 14	28	
Gate resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	 2	4	Ω

## **Drain-Source Diode Characteristics and Maximum Ratings**

Parameter	Symbol	nbol Conditions		Тур.	Max.	Unit
Continuous Source Current	ls	\/-=\/-=0\/			3	Α
Pulsed Source Current	Ism	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			6	Α
Diode Forward Voltage	$V_{SD}$	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>G</sub> s=0V,I <sub>S</sub> =1A , di/dt=100A/		70		ns
Reverse Recovery Charge	Qrr	µs T <sub>J</sub> =25℃		114		nC

#### Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. V<sub>DD</sub>=50V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=11A.,R<sub>G</sub>=25Ω,Starting T<sub>J</sub>=25°C.
- The data tested by pulsed , pulse width  $\leqq 300 us$  , duty cycle  $\leqq 2\%.$
- 4. Essentially independent of operating temperature.



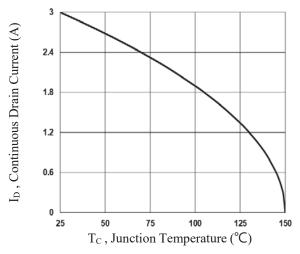


Fig.1 Continuous Drain Current vs. Tc

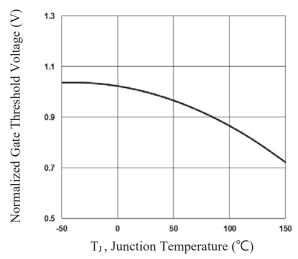


Fig.3 Normalized Vth vs. TJ

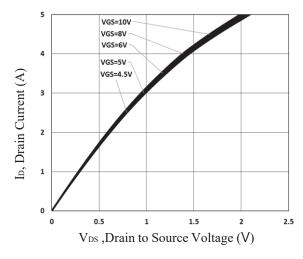


Fig.5 Typical Output Characteristics

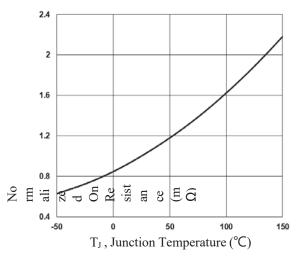


Fig.2 Normalized RDSON vs. TJ

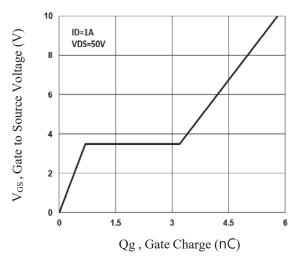


Fig.4 Gate Charge Waveform

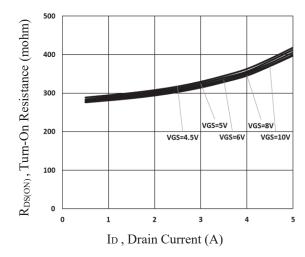


Fig.6 Turn-On Resistance vs. ID

http://www.hfzt.net 2017.6-Rev.A



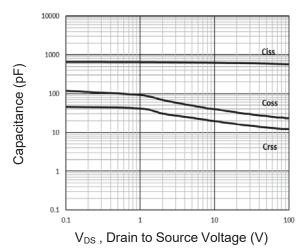


Fig.7 Capacitance Characteristics

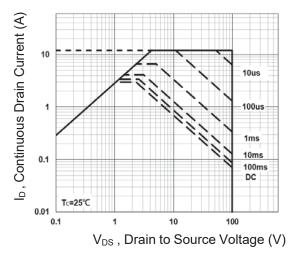


Fig.9 Maximum Safe Operation Area

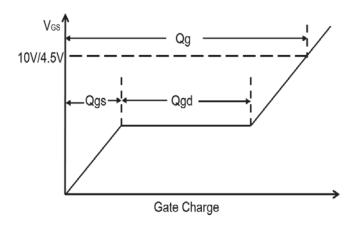


Fig.11 Gate Charge Waveform

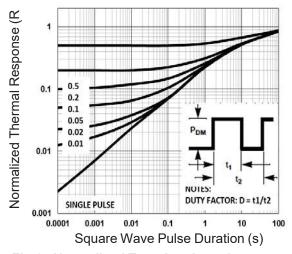


Fig.8 Normalized Transient Impedance

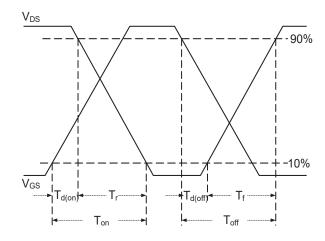
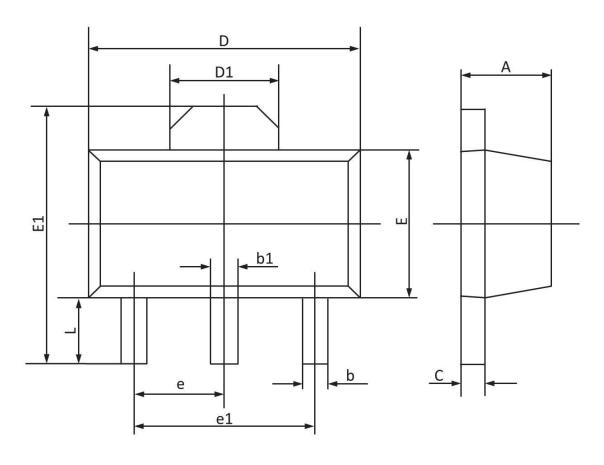


Fig.10 Switching Time Waveform



# **SOT89 PACKAGE INFORMATION**



Crussla al	Dimensions	In Millimeters	<b>Dimensions In Inches</b>		
Symbol	Min	Max	Min	Max	
A	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.020	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550	1.550 REF		REF	
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
e	1.500	TYP	0.060	TYP	
e1	3.000	TYP	0.118	TYP	
L	0.900	1.200	0.035	0.047	

http://www.hfzt.net 2017.6-Rev.A